

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Chen et al.		
Title:	Read and Erase Verify Methods and Circuits Suitable for Low Voltage Non-Volatile Memories		
Application No.:	10/552,948	Filing Date:	April 8, 2004
Examiner:	Yoha, Connie C.	Group Art Unit:	2827
Docket No.:	SNDK.284US1	Conf. No.:	6573

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Mail Stop Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

**AMENDMENT**

Dear Sir:

This Amendment is responsive to the Official Action mailed on July 16, 2007. Applicant responds to the Official Action as follows:

**A Listing of the Claims**, including amendments, begins on page 2 of this paper.

**Remarks** begin on page 5 of this paper.